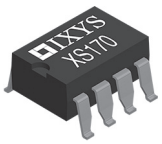
IXYS
INTEGRATED CIRCUITS DIVISION



XS170
Multifunction Telecom Switch

Parameter	Rating	Units
Blocking Voltage	350	V_P
Load Current	100	mA_{rms} / mA_{DC}
On-Resistance (max)	50	Ω

Features
- 3750V_{rms} Input/Output Isolation
- Low Drive Power Requirements
- FCC Compatible
- VDE Compatible
- High Reliability
- Arc-Free With No Snubbing Circuits
- No EMI/RFI Generation
- Small 8-Pin Package
- Surface Mount Tape & Reel Version Available
- Flammability Rating UL 94 V-0

Applications
- Telecommunications
  - Telecom Switching
  - Tip/Ring Circuits
  - Modem Switching (Laptop, Notebook, Pocket Size)
  - Hook Switch
  - Dial Pulsing
  - Ground Start
  - Ringing Injection
- Instrumentation
- Multiplexers
- Data Acquisition
- Electronic Switching
- I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls

Description
The XS170 integrated circuit device combines a 350V, 100mA, 50Ω, normally open (1-Form-A) relay with an optocoupler in a single package. The relay uses optically coupled MOSFET technology to provide 3750V_{rms} of input to output isolation.

Its optically coupled outputs, which use the patented OptoMOS architecture, are controlled by a highly efficient infrared LED.

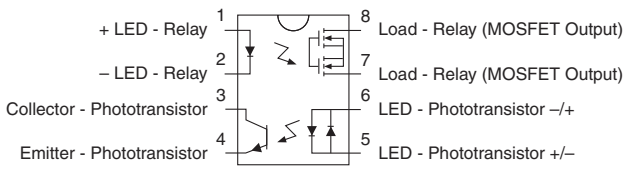
Telecom circuit designers, using the XS170, can now take advantage of two discrete functions in a single component that uses less space than traditional discrete component solutions.

Approvals
- UL Recognized Component: File E76270
- EN/IEC 60950-1 Certified Component:
  TUV Certificate: B 13 12 82667 003

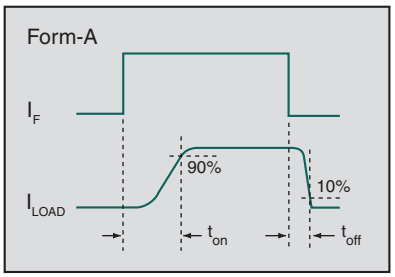
Ordering Information

Part #	Description
XS170	8-Pin DIP (50/Tube)
XS170S	8-Pin Surface Mount (50/Tube)
XS170STR	8-Pin Surface Mount (1000/Reel)

Pin Configuration



Switching Characteristics of Normally Open Devices



ПРОМЭЛЕКТРОНИКА / sales@prom-elec.com

Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Relay Blocking Voltage	350	V_P
Reverse Input Voltage	5	V
Input Power Dissipation [1]	150	mW
Relay Input Control Current	50	mA
Peak (10ms)	1	A
Detector Input Control Current	100	mA
Total Power Dissipation [2]	800	mW
Isolation Voltage, Input to Output	3750	V_{rms}
Operational Temperature (T_A)	-40 to +85	°C
Storage Temperature	-40 to +125	°C

[1] Derate linearly 1.33 mW / °C
[2] Derate linearly 6.67 mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

Electrical Characteristics @25°C: Relay Section

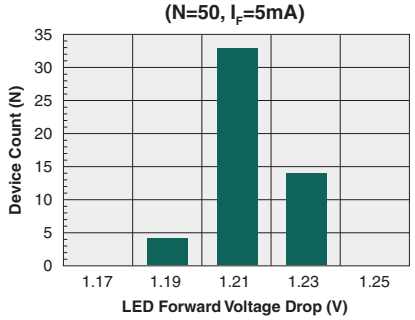
Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Load Current, Continuous						
Continuous	-	I_L	-	-	100	mA_{rms} / mA_{DC}
Peak	t=10ms	I_{LPK}	-	-	±350	mA_P
On-Resistance	I_L=120mA	R_{ON}	-	33	50	Ω
Off-State Leakage Current	V_L=350V	I_{LEAK}	-	-	1	µA
Switching Speeds						
Turn-On	I_F=5mA, V_L=10V	t_{on}	-	-	5	ms
Turn-Off		t_{off}	-	-	5	
Output Capacitance	I_F=0mA, V_L=50V, f=1MHz	C_{OUT}	-	25	-	pF
Input Characteristics						
Input Control Current to Activate	I_L=120mA	I_F	-	-	2	mA
Input Control Current to Deactivate	-	I_F	0.4	0.7	-	mA
Input Voltage Drop	I_F=5mA	V_F	0.9	1.2	1.4	V
Reverse Input Current	V_R=5V	I_R	-	-	10	µA
Common Characteristics						
Capacitance, Input to Output	V_{IO}=0V, f=1MHz	C_{IO}	-	3	-	pF

Electrical Characteristics @25°C: Detector Section

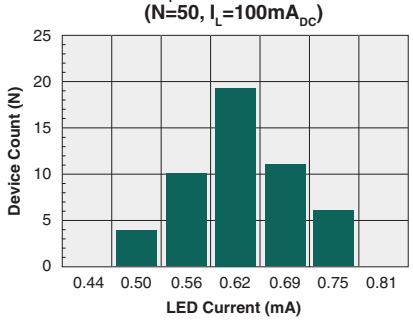
Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Phototransistor Blocking Voltage	I_C=10µA	BV_{CEO}	20	50	-	V
Phototransistor Dark Current	V_{CE}=5V, I_F=0mA	I_{CEO}	-	50	500	nA
Saturation Voltage	I_C=2mA, I_F=16mA	V_{SAT}	-	0.3	0.5	V
Current Transfer Ratio	I_F=6mA, V_{CE}=0.5V	CTR	33	100	-	%
Input Characteristics						
Input Control Current	I_C=2mA, V_{CE}=0.5V	I_F	-	2	6	mA
Input Voltage Drop	I_F=5mA	V_F	0.9	1.2	1.4	V
Input Current (Detector must be off)	I_C=1µA, V_{CE}=5V	I_F	5	25	-	µA
Isolation, Input to Output	-	$V_{I/O}$	3750	-	-	V_{rms}
Common Characteristics						
Input to Output Capacitance	-	$C_{I/O}$	-	3	-	pF

RELAY PERFORMANCE DATA*

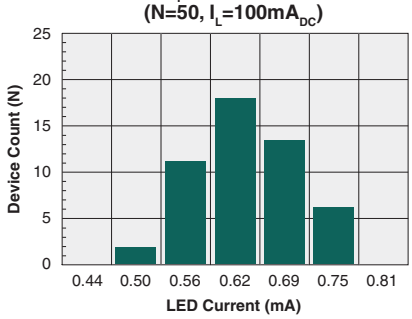
Typical LED Forward Voltage Drop (N=50, I_F=5mA)



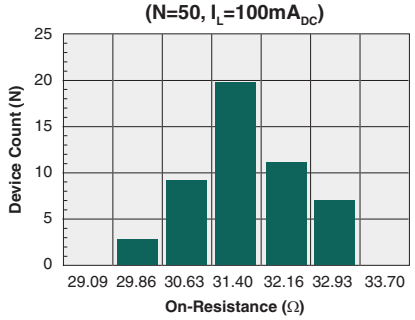
Typical I_F for Switch Operation (N=50, I_L=100mA$_{DC}$)



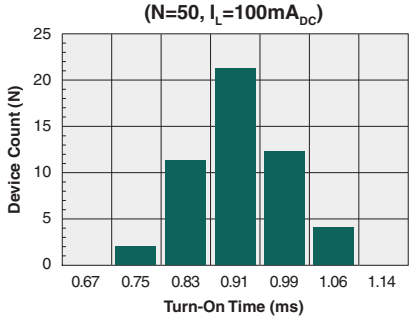
Typical I_F for Switch Dropout (N=50, I_L=100mA$_{DC}$)



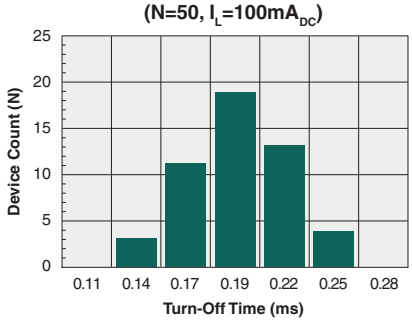
Typical On-Resistance Distribution (N=50, I_L=100mA$_{DC}$)



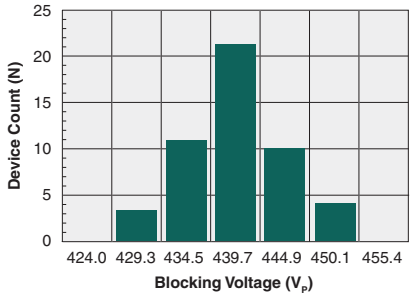
Typical Turn-On Time (N=50, I_L=100mA$_{DC}$)



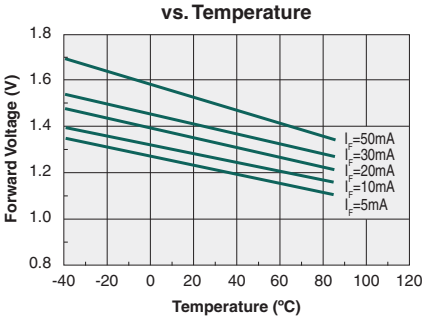
Typical Turn-Off Time (N=50, I_L=100mA$_{DC}$)



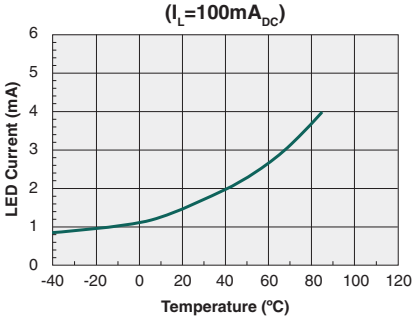
Typical Blocking Voltage Distribution (N=50)



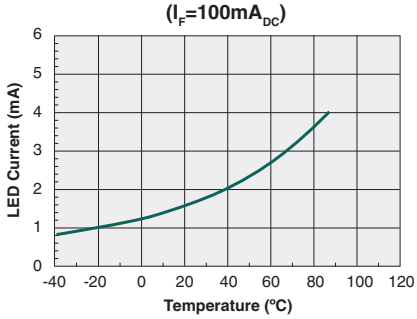
Typical LED Forward Voltage Drop vs. Temperature



Typical I_F for Switch Operation vs. Temperature (I_L=100mA$_{DC}$)



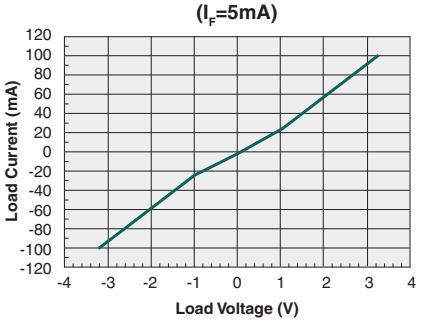
Typical I_F for Switch Dropout vs. Temperature (I_F=100mA$_{DC}$)



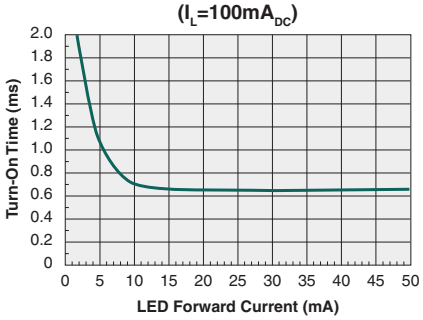
* Unless otherwise noted, data presented in these graphs is typical of device operation at 25ºC.
For guaranteed parameters not indicated in the written specifications, please contact our application department.

RELAY PERFORMANCE DATA*

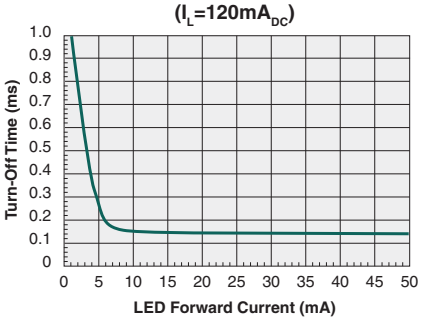
Typical Load Current vs. Load Voltage (I_F=5mA)



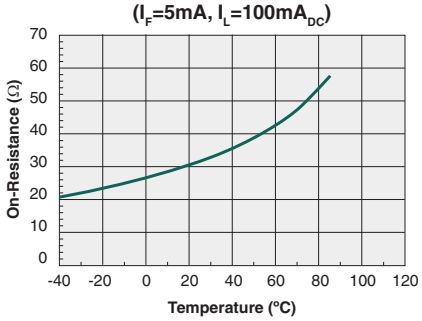
Typical Turn-On Time vs. LED Forward Current (I_L=100mA$_{DC}$)



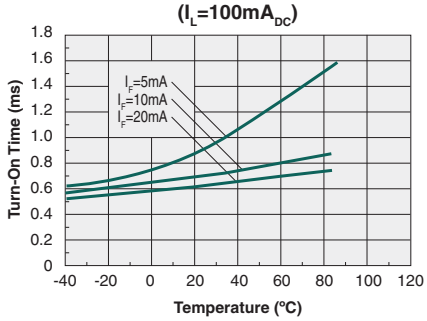
Typical Turn-Off Time vs. LED Forward Current (I_L=120mA$_{DC}$)



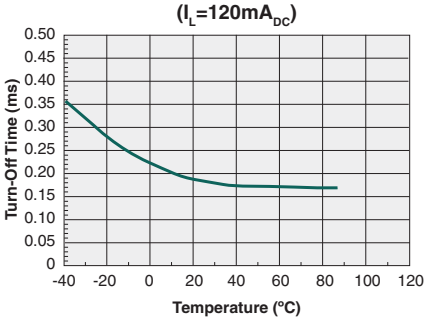
Typical On-Resistance vs. Temperature (I_F=5mA, I_L=100mA$_{DC}$)



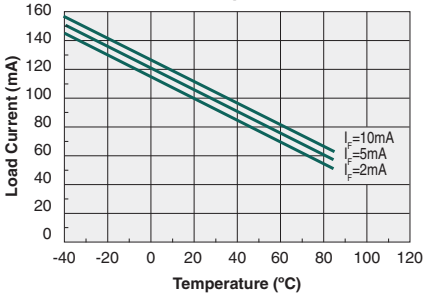
Typical Turn-On Time vs. Temperature (I_L=100mA$_{DC}$)



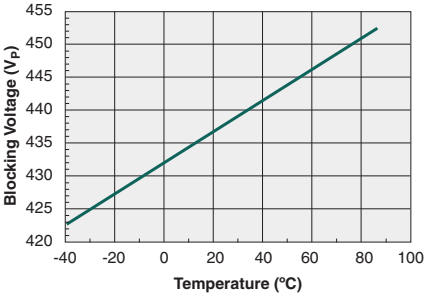
Typical Turn-Off Time vs. Temperature (I_L=120mA$_{DC}$)



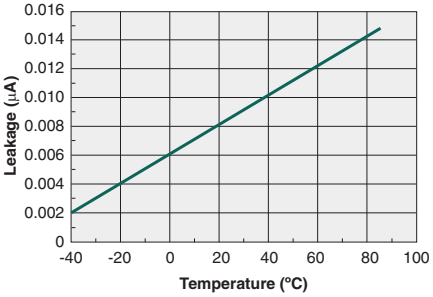
Maximum Load Current vs. Temperature



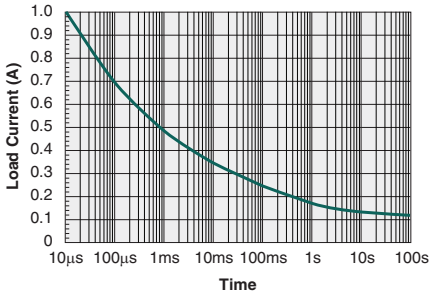
Typical Blocking Voltage vs. Temperature



Typical Leakage vs. Temperature Measured across Pins 4&6



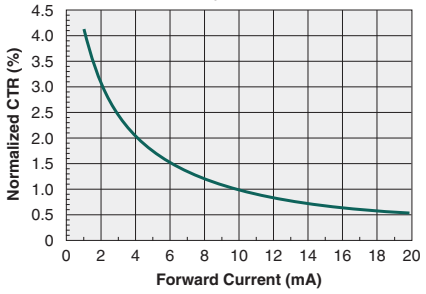
Energy Rating Curve



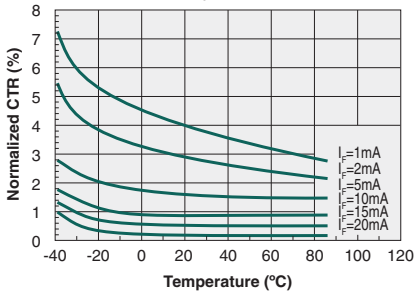
* Unless otherwise noted, data presented in these graphs is typical of device operation at 25ºC.
For guaranteed parameters not indicated in the written specifications, please contact our application department.

DETECTOR PERFORMANCE DATA*

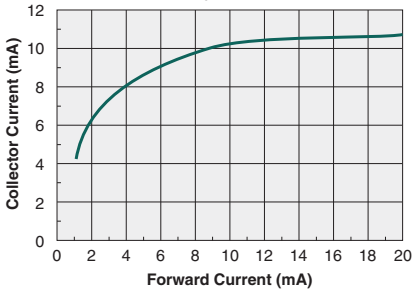
Typical Normalized CTR vs. Forward Current (V_{CE}=0.5V)



Typical Normalized CTR vs. Temperature (V_{CE}=0.5V)



Typical Collector Current vs. Forward Current (V_{CE}=0.5V)



* Unless otherwise noted, data presented in these graphs is typical of device operation at 25ºC.
For guaranteed parameters not indicated in the written specifications, please contact our application department.

Manufacturing Information

Moisture Sensitivity

All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits Division classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a Moisture Sensitivity Level (MSL) classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Classification
XS170 / XS170S	MSL 1

ESD Sensitivity

This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

Soldering Profile

Provided in the table below is the Classification Temperature (T_C) of this product and the maximum dwell time the body temperature of this device may be (T_C - 5)ºC or greater. The classification temperature sets the Maximum Body Temperature allowed for this device during lead-free reflow processes. For through-hole devices, and any other processes, the guidelines of **J-STD-020** must be observed.

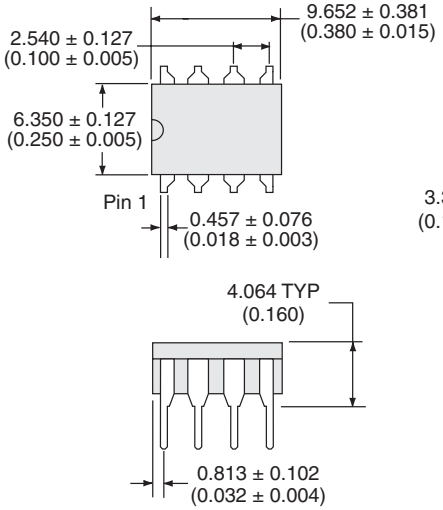
Device	Classification Temperature (T_c)	Dwell Time (t_p)	Max Reflow Cycles
XS170	250ºC	30 seconds	-
XS170S	250ºC	30 seconds	3

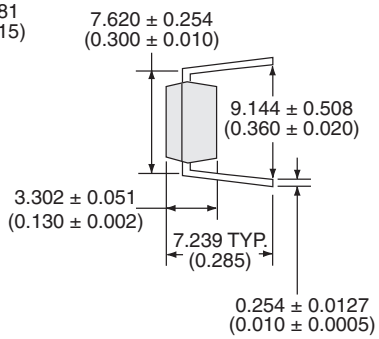
Board Wash

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include, but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to flux or solvents that are Chlorine- or Fluorine-based.

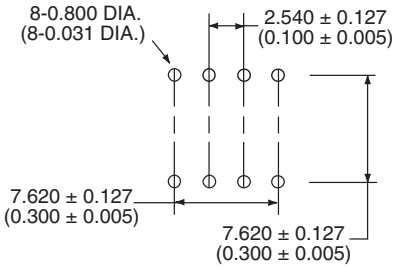
Mechanical Dimensions

XS170



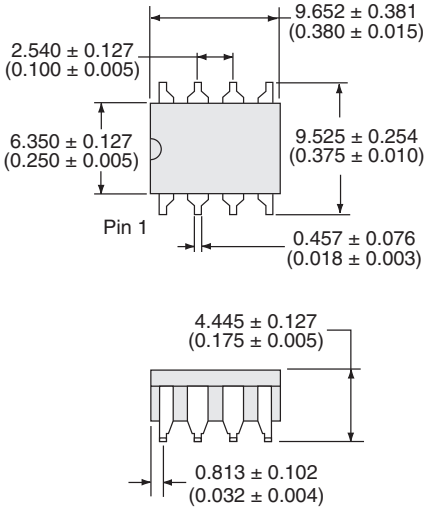


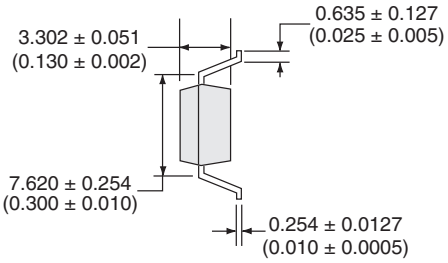
PCB Hole Pattern



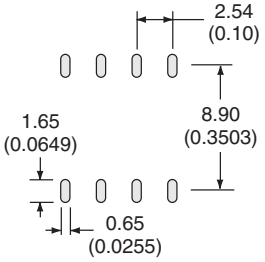
Dimensions
mm
(inches)

XS170S



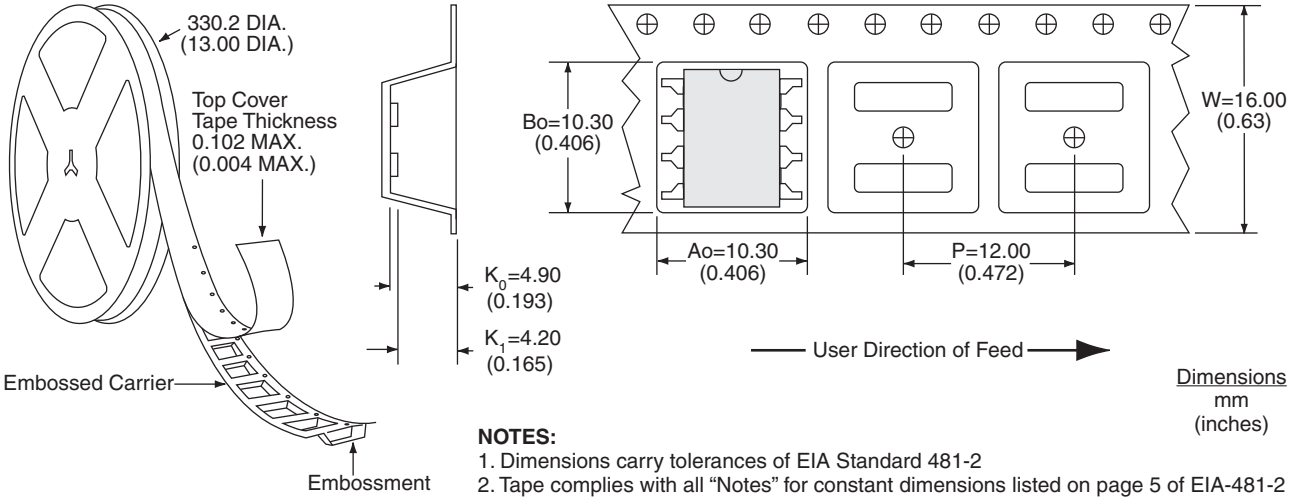


PCB Land Pattern



Dimensions
mm
(inches)

XS170STR Tape & Reel



NOTES:
1. Dimensions carry tolerances of EIA Standard 481-2
2. Tape complies with all “Notes” for constant dimensions listed on page 5 of EIA-481-2

For additional information please visit our website at: www.ixysic.com

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6/19/2017

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