

Parameter	Rating	Units
Blocking Voltage	400	V _P
Load Current	250	mA _{rms} / mA _{DC}
On-Resistance (max)	8	Ω

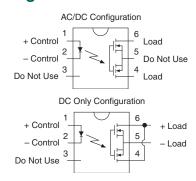
Features

- · Low On-Resistance, High Current Handling
- Low Drive Power Requirements (TTL/CMOS Compatible)
- ullet 3750V $_{
 m rms}$ Input/Output Isolation
- High Reliability
- VDE Compatible
- FCC Compatible
- · Arc-Free With No Snubbing Circuits
- No EMI/RFI Generation
- · Machine Insertable, Wave Solderable
- Small 6-Pin Package
- Surface Mount Tape & Reel Version Available

Applications

- Telecommunications
 - Telecomm Switching
 - Hook Switch
- Instrumentation
 - Multiplexers
 - Data Acquisition
 - Electronic Switching
 - I/O Subsystems
- · Meters (Watt-Hour, Water, Gas)
- · Medical Equipment—Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls
- Automotive

Pin Configuration









Description

The PLA140 is a single-pole normally open (1-Form-A) Solid State Relay that uses optically coupled MOSFET technology to provide 3750V_{rms} of input-to-output isolation.

MOSFET output switches, which use IXYS Integrated Circuits Division's patented OptoMOS architecture, are controlled by a highly efficient GaAIAs infrared LED.

The PLA140's combination of low on-resistance and high load current handling makes it suitable for a variety of industrial applications.

Because Solid State Relays like the PLA140 have no moving parts, they offer faster, bounce-free switching in a more compact surface mount or though hole package than traditional electromechanical relays.

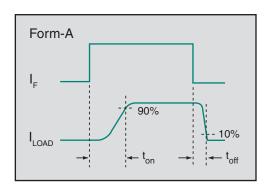
Approvals

- UL Certified Component: File E76270
- CSA Certified Component: Certificate 1175739
- Certified to EN60950 TUV Certificate B 09 07 49410 004

Ordering Information

Part Number	Description
PLA140	6-Pin DIP (50/Tube)
PLA140S	6-Pin Surface Mount (50/Tube)
PLA140STR	6-Pin Surface Mount (1,000/Reel)

Switching Characteristics of Normally Open Devices





Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	400	V_P
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	Α
Input Power Dissipation ¹	150	mW
Total Power Dissipation ²	800	mW
Isolation Voltage, Input to Output	3750	V_{rms}
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Electrical Characteristics @ 25°C

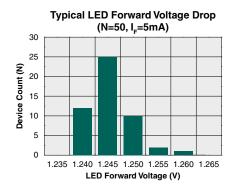
Parameter	Conditions	Symbol	Min	Тур	Max	Units
Output Characteristics					'	
Load Current (Continuous)						
AC/DC Configuration	I 5 A		-	-	250	mA_{rms} / mA_{DC}
DC Configuration	I _F =5mA	l l	-	-	350	mA _{DC}
Peak Load Current	I _F =5mA , t=10ms	I _{LPK}	-	-	±500	mA _P
On-Resistance						
AC/DC Configuration	I _F =5mA , I _L =250mA	D	-	5.5	8	Ω
DC Configuration	I _F =5mA , I _L =350mA	R _{ON}	-	1.5	3	2.2
Off-State Leakage Current	V _L =400V _P	I _{LEAK}	-	-	1	μΑ
Switching Speeds						
Turn-On	I Em A \/ 10\/	t _{on}	-	0.4	3	
Turn-Off	$I_F=5mA, V_L=10V$	t _{off}	-	0.19	1	ms
Output Capacitance	I _F =0mA , V _L =50V, f=1MHz		-	18	-	pF
Input Characteristics	1					
Input Control Current to Activate	I _L =250mA	l _F	-	0.46	5	mA
Input Control Current to Deactivate	-	I _F	0.2	0.44	-	mA
Input Voltage Drop	I _F =5mA	V_{F}	0.9	1.2	1.4	V
Reverse Input Current	V _R =5V	I _R	-	-	10	μΑ
Common Characteristics	<u>'</u>	1		1		
Input to Output Capacitance	-	C _{I/O}	-	3	-	pF

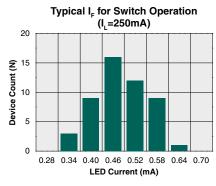
¹ Derate linearly 1.33 mW / °C

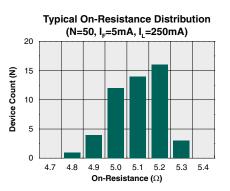
² Derate linearly 6.67 mW / °C

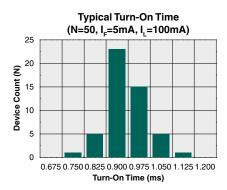


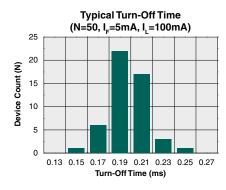
PERFORMANCE DATA*

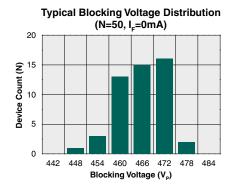


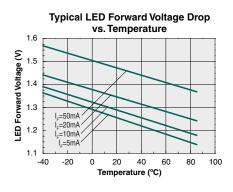


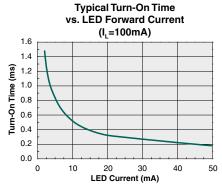


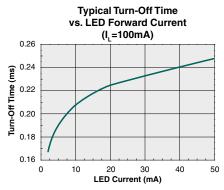


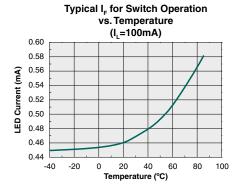


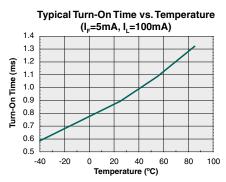


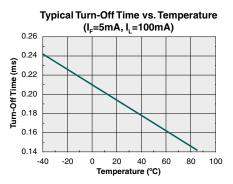








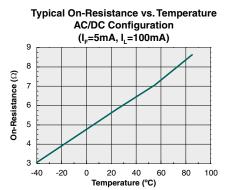


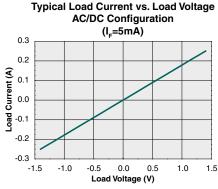


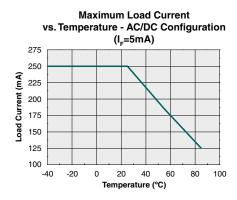
^{*}The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

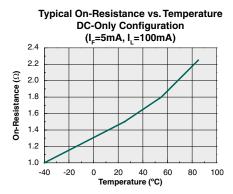


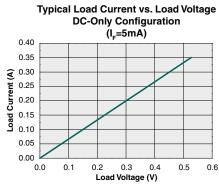
PERFORMANCE DATA*

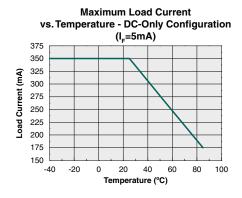


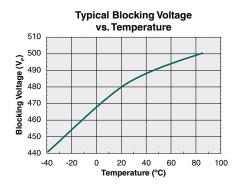


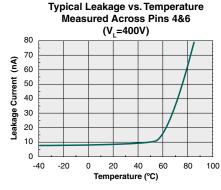


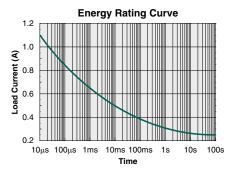












^{*}The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.



Manufacturing Information

Moisture Sensitivity

All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, IPC/JEDEC J-STD-020, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating
PLA140 / PLA140S	MSL 1

ESD Sensitivity



This product is ESD Sensitive, and should be handled according to the industry standard JESD-625.

Reflow Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

Device	Maximum Temperature x Time	
PLA140 / PLA140S	250°C for 30 seconds	

Board Wash

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.



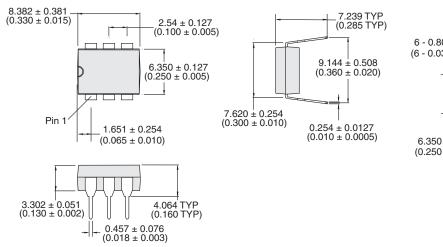




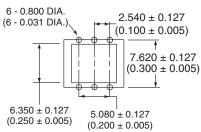


MECHANICAL DIMENSIONS

PLA140

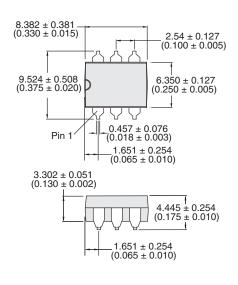


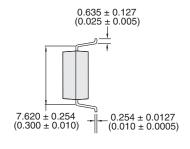
PCB Hole Pattern

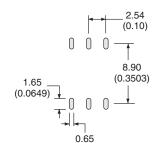


Dimensions mm (inches)

PLA140S







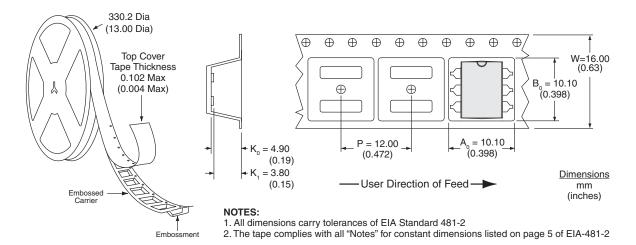
(0.0255)

PCB Land Pattern

Dimensions mm (inches)



PLA140STR Tape & Reel



For additional information please visit our website at: www.ixysic.com

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