



Parameter	Rating	Units
Blocking Voltage	60	V_P
Load Current	± 6	A_{DC}
On-Resistance	0.06	Ω

Features

- Handle Load Currents Up to $\pm 6A_{DC}$ or $6A_{rms}$
- 5000V_{rms} Input/Output Isolation
- Power SOIC Package
- 12.5mm External Creepage Distance with Appropriate Layout
- High Reliability
- No Moving Parts
- Low Drive Power Requirements (TTL/CMOS Compatible)
- Arc-Free With No Snubbing Circuits
- No EMI/RFI Generation

Applications

- Industrial Controls
- Security: Door Latches, Solenoids, Annunciators
- Motor Control
- Heating, Ventilation, and Air Conditioning Control (HVAC)
- Robotics
- Starter Ignition Circuits
- Medical Equipment—Patient/Equipment Isolation
- Instrumentation
 - Multiplexers
 - Electronic Switching
 - I/O Subsystems
- Home Appliances
- DC Power Supplies
- Aerospace

Description

The CPC1907B is a single-pole, normally open (1-Form-A) solid state relay that employs optically coupled MOSFET technology to provide 5000V_{rms} of input to output isolation.

Switching of the efficient MOSFET switches is controlled by the photovoltaic die using the patented OptoMOS architecture while activation of the output is controlled by a highly efficient GaAlAs infrared LED. The combination of low on-resistance and high load current handling capabilities makes the relay suitable for a variety of high-performance switching applications.

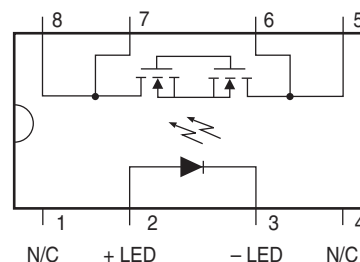
Approvals

- UL 508 Certified Component: File E69938
- CSA Industrial Control Switches Approval: Pending

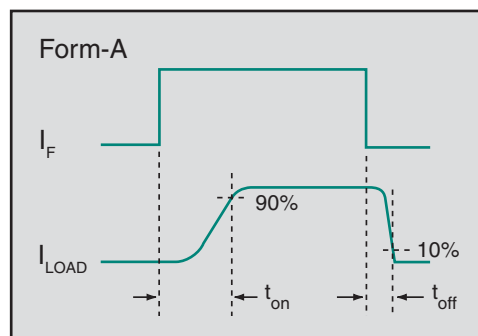
Ordering Information

Part #	Description
CPC1907B	8-Pin Power SOIC Package (25 per tube)

Pin Configuration



Switching Characteristics of Normally Open Devices



Absolute Maximum Ratings @ 25°C (Unless Otherwise Noted)

Parameter	Ratings	Units
Blocking Voltage	60	V
Reverse Input Voltage	5	V
Input control Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation ¹	150	mW
Total Power Dissipation ²	2400	mW
Isolation Voltage, Input to Output (60 Seconds Maximum)	5000	V _{rms}
ESD, Human Body Model	8	kV
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

¹ Derate linearly 3.33mW / °C

² Derate linearly 20mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

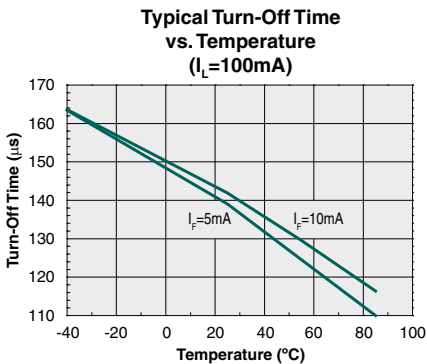
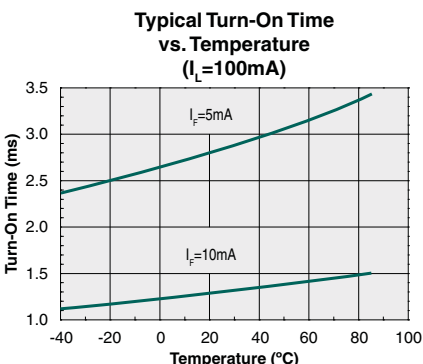
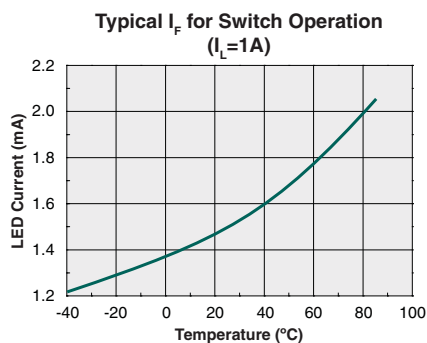
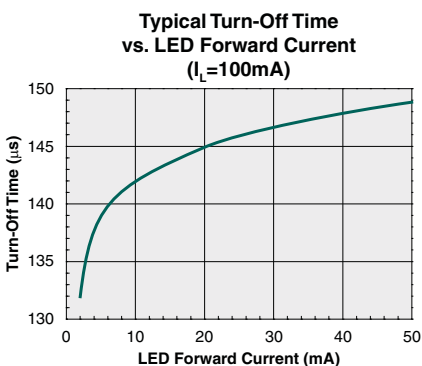
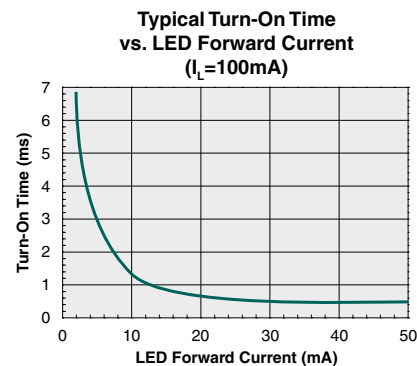
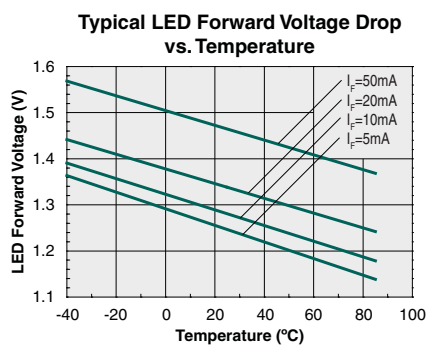
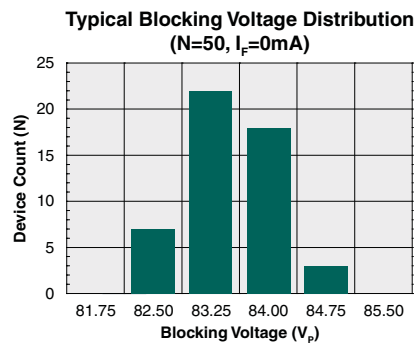
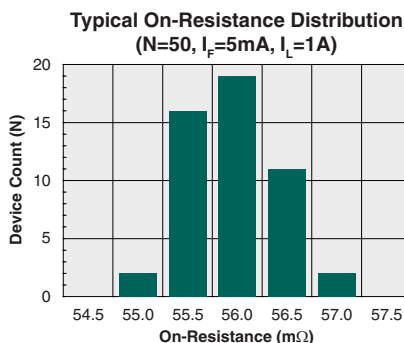
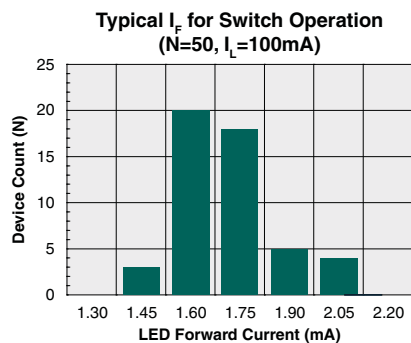
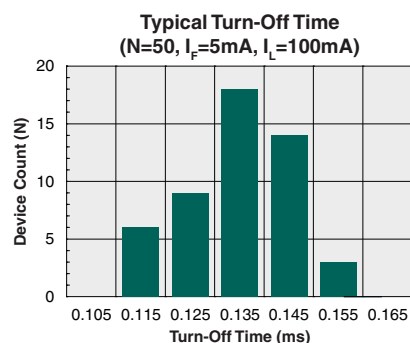
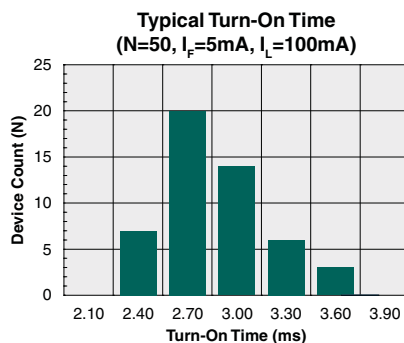
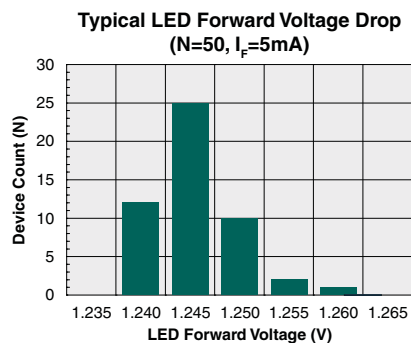
Electrical Characteristics @ 25°C (Unless Otherwise Noted)

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Load Current, Continuous	free air	I _L	-	-	6	±A _{DC} , A _{rms}
Peak Load Current	t = 10ms	I _{LPK}	-	-	20	±A _P
On-Resistance ¹	I _L =1A	R _{ON}	-	-	0.06	Ω
Off-State Leakage Current	V _L =60V _P	I _{LEAK}	-	-	1	μA
Switching Speeds	I _F =5mA, I _L =100mA	t _{on}	-	2.7	5	ms
Turn-On		t _{off}	-	0.14	1	
Turn-Off						
Output Capacitance	V _L =50V, f=1MHz	C _{OUT}	-	340	-	pF
Input Characteristics						
LED Current to Activate ²	I _L =1A	I _F	-	1.5	5	mA
LED Current to Deactivate	-	I _F	0.6	-	-	mA
Input Voltage Drop	I _F =5mA	V _F	0.9	1.2	1.4	V
Reverse Input Current	V _R =5V	I _R	-	-	10	μA
Input/Output Characteristics						
Capacitance, Input/Output	f=1MHz	I _{I/O}	-	2	-	pF

¹ Measurement taken within 1 second of on-time.

² For high temperature operation (T_A>60°C), a LED current of 10mA is recommended.

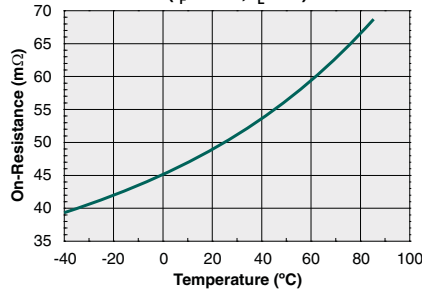
PERFORMANCE DATA @25°C (Unless Otherwise Noted)*



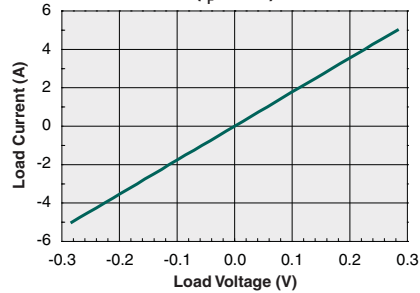
*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA @25°C (Unless Otherwise Noted)*

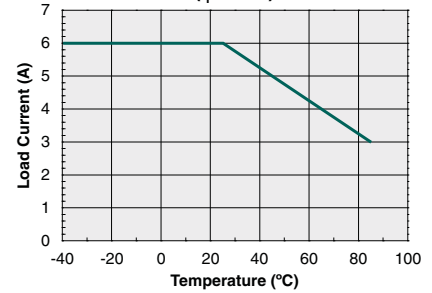
**Typical On-Resistance
vs. Temperature
($I_F=5\text{mA}$, $I_L=1\text{A}$)**



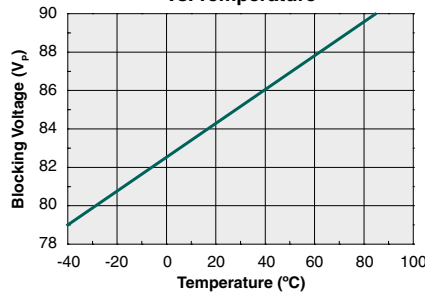
**Typical Load Current vs. Load Voltage
($I_F=5\text{mA}$)**



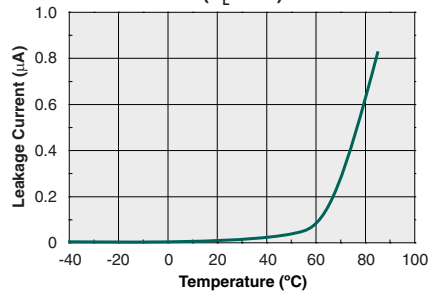
**Maximum Load Current
vs. Temperature
($I_F=5\text{mA}$)**



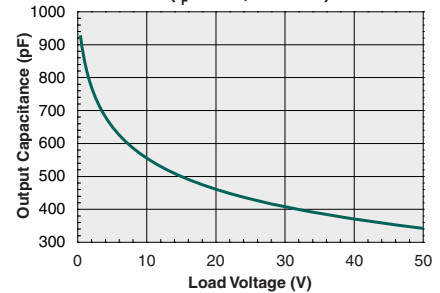
**Typical Blocking Voltage
vs. Temperature**



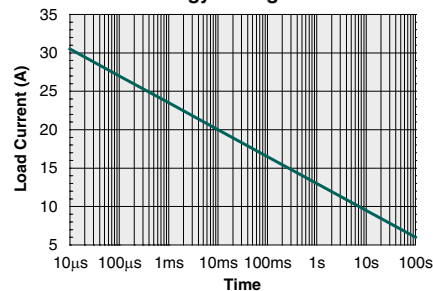
**Leakage Current vs. Temperature
Across Pins 5&6, 7&8
($V_L=60\text{V}$)**



**Output Capacitance vs. Load Voltage
($I_F=0\text{mA}$, $f=1\text{MHz}$)**



Energy Rating Curve



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

Manufacturing Information

Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating
CPC1907B	MSL 1

ESD Sensitivity



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

Reflow Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

Device	Maximum Temperature x Time
CPC1907B	245°C for 30 seconds

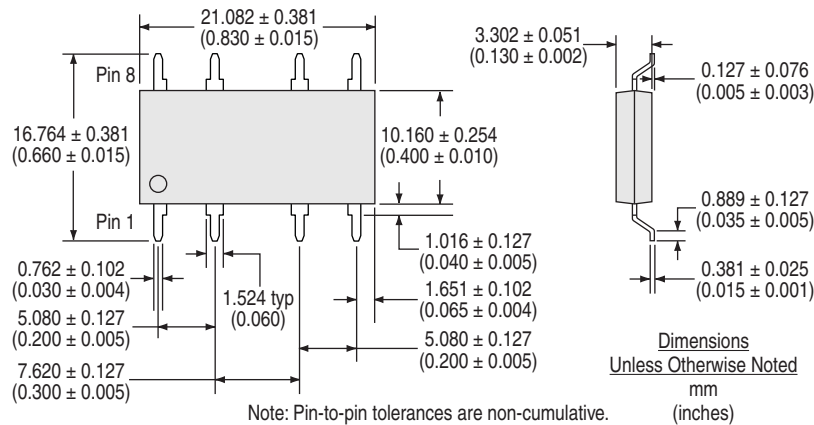
Board Wash

IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.

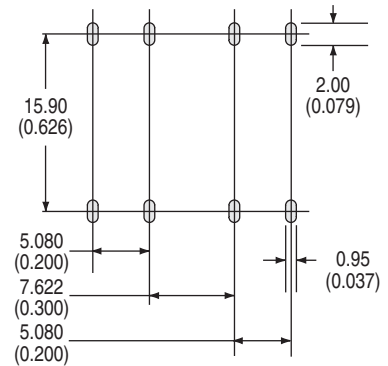


MECHANICAL DIMENSIONS

CPC1907B



Recommended PCB Pattern



For additional information please visit our website at: www.ixysic.com

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